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#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

Akio KITAMURA

Serial No.: 10/055,722

Group Art Unit: 2811

Filed: January 23, 2002

Examiner:

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND

MANUFACTURE METHOD THEREFORE

#### Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450 on:

N3 131 /A

By:

Marc A. Rossi

## REQUEST FOR ACKNOWLEDGMENT OF INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

Applicant hereby requests that the Patent Office acknowledge the receipt of the Information Disclosure Statement filed on January 23, 2002 for the above-referenced application. For the convenience of the Patent Office, a copy of the Information Disclosure Statement, the PTO Form-1449 and the stamped Receipt Card are enclosed. It is respectfully requested that an initialed copy of the PTO-1449 be returned to the undersigned.

Respectfully submitted,

**ROSSI & ASSOCIATES** 

03/31/69

Date

Marc A. Rossi

Registration No. 31,923

Attorney Docket No. FUJI:203

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Akio KITAMURA

Sorial No : NEW ADDI ICATION TRADE

Serial No.: NEW APPLICATION

Filed: January 23, 2002

Batch:

Group Art Unit:

Examiner:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND

MANUFACTURE METHOD THEREFORE

#### INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

For:

Pursuant to 37 C.F.R. §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. Copies of the references listed on Form PTO-1449 are attached.

It is respectfully requested that the information be expressly considered during the prosecution of this application, that these references be made of record therein and appear among the "References Cited" on any patent to issue therefrom, and that an initialed copy of the PTO-1449 be returned to the undersigned.

Respectfully submitted,

Date: \_\_\_\_\_01/23/07\_

Marc A. Rossi

Registration No. 31,923

Attorney Docket No.: FUJI:203

**ROSSI & ASSOCIATES** 

P.O. Box 826

Ashburn, VA 20146-0826

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					Docket Number (Optional) FUJI:20		Application Number NEW APPLICATION			
					Applicant(s) Akio KITAMURA					
APR 0 1 2004 (5)					Filing Date Group Art Unit  January 23, 2002					
				U.S. PAT	ENT DOCUMENTS	· · · · · · · · · · · · · · · · · · ·				
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	REF	DOCUMENT NUMBER	DATE	JAPAN (English Abst.)  JAPAN (English Abst.)		CLASS	SUBCLASS	Tran YES	nslation NO	
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	JP	60-105277	6/85					J		
	JP	61-190983	8/86	JAPAN	(English Abst.)			1		
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Form PTO-A820 (also form PTO-1449)



USPT RECEIPT CARD In re application of:

Akio KITAMURA

Batch:

Serial No.: NEW APPLICATION Group Art Unit:

Filed: January 23, 2002

Examiner:

SEMICONDUCTOR INTEGRATED CIRCUIT For: DEVICE AND MANUFACTURE METHOD

**THEREFORE** 

Papers Filed: NEW APPLICATION; SPECS. 20 PGS.; FIGS. 1-14; IDS; PRIORITY

Attorney Docket: FUJI:203